Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	413278	liquid crystal display or LCD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:00
L2	34961	source adj driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:00
L3	172532	voltage near3 driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:00
L4	25687	gate adj driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:00
L5	968570	scan\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:00
L6	749474	invert\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:00
L7	905638	buffer\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:00
L8	317	L1 and L2 and L3 and L4 and L5 and L6 and L7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:00
L9	20	chip adj on adj glass\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:00
L10	2386	panel adj wir\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:01

L11	1569	contrast adj voltage\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:01
L12	23386	charge adj pump\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:01
L13	413278	liquid crystal display or LCD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:02
L14	34961	source adj driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:02
L15	172532	voltage near3 driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:02
L16	25687	gate adj driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:02
L17	2386	panel adj wir\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:02
L18	22	L13 and L14 and L15 and L16 and L17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:02
L19	968570	scan\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:03
L20	1569	contrast adj voltage\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:03
L21	1	L13 and L14 and L15 and L16 and L17 and L19 and L20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:03

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L24	23386	charge adj pump\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:04
L25	2	L13 and L14 and L15 and L16 and L17 and L19 and L24	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:04
L26	410	buffer circuit same stabiliz\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:04
L27	1	L13 and L14 and L15 and L16 and L17 and L19 and L26	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:04
L28	143838	CMOS or Complementary Metal Oxide Semiconductors	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:05
L29	1	L13 and L14 and L15 and L16 and L17 and L19 and L28	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:05
L30	49886	( CMOS or Complementary Metal Oxide Semiconductor\$3) and invert\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:05
L31	1	L13 and L14 and L15 and L16 and L17 and L19 and L30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:05
S1	20	chip adj on adj glass\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:00
S2	15574	chip\$3 near6 glass\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 14:48
S3	372784	liquid crystal display or LCD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:17

S4	20821	LCD panel	US-PGPUB;	ADJ	ON	2005/09/21 15:18
34	20021	Lob panel	USPAT; EPO; JPO; DERWENT; IBM_TDB	7.50		2333/33/21 10:10
S5	32049	source adj driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:19
S6	158995	voltage near3 driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:18
S7	23100	gate adj driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:20
S8	2233	panel adj wir\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:01
S9	900057	scan\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:21
S10	1428	contrast adj voltage\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:01
S11	21144	charge adj pump\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:01
S12	386	buffer circuit same stabiliz\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:14
S13	130629	CMOS or Complementary Metal Oxide Semiconductors	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:27
S14	12027	( CMOS or Complementary Metal Oxide Semiconductor\$3) adj inverters	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:21

S15	108421	resistance value	US-PGPUB; USPAT; EPO; JPO; DERWENT;	ADJ	ON	2005/09/21 15:50
S16	2410	LCOS	IBM_TDB US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:51
S17	1040	S3 and S5 and S6 and S7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 15:51
S18	17	S3 and S5 and S6 and S7 and S8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:02
S19	12	S3 and S5 and S6 and S7 and S8 and S9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:16
S20	1	S3 and S5 and S6 and S7 and S8 and S9 and S10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:03
S21	0	S3 and S5 and S6 and S7 and S8 and S9 and S16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:11
S22	2	S3 and S5 and S6 and S7 and S8 and S9 and S11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:04
S23	1	S3 and S5 and S6 and S7 and S8 and S9 and S12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:04
S24	1	S3 and S5 and S6 and S7 and S8 and S9 and S13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:05
S25	2899	buffer circuit and stabiliz\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:15

S26	1	S3 and S5 and S6 and S7 and S8 and S9 and S25	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:22
S27	130294	buffer\$6 and stabiliz\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:15
S28	1	S3 and S5 and S6 and S7 and S8 and S9 and S27	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:16
S29	1	S3 and S5 and S6 and S7 and S8 and S9 and S4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:16
S30	46442	( CMOS or Complementary Metal Oxide Semiconductor\$3) and invert\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:22
S31	1	S3 and S5 and S6 and S7 and S8 and S9 and S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:05
S32	713471	invert\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:22
S33	8	S3 and S5 and S6 and S7 and S8 and S9 and S32	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:26
S34	843382	buffer\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/09/21 16:23
S35	4	S3 and S5 and S6 and S7 and S8 and S9 and S32 and S34	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:46
S36	384671	liquid crystal display or LCD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:45

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S37	32944	source adj driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:45
S38	162911	voltage near3 driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:45
S39	23846	gate adj driv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:45
S40	919994	scan\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:45
S41	723926	invert\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:45
S42	862057	buffer\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:45
S43	270	S36 and S37 and S38 and S39 and S40 and S41 and S42	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:45
S44	270	S36 and S37 and S38 and S39 and S40 and S41 and S42	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/05/26 14:00
S45	18333	(chip adj on adj glass\$3 or COG or LCOS)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:48
S46	37	S44 and S45	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/01 09:48



Day: Friday Date: 5/26/2006

Time: 13:31:19

#### **Inventor Name Search Result**

Your Search was:

Last Name = CHUNG

First Name = KYUNG HOON

Applicati	on#	Patent#	Status	Date Filed	Title	Inventor Name
106207	<u>16</u>	Not Issued	71	1	Chip-on-glass type liquid crystal display	CHUNG, KYUNG HOON

Inventor Search Completed: No Records to Display.

	Last Name	First Name	
Search Another: Inventor	CHUNG	KYUNG HOON	Search
	Onora	J. C.	

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#### **Inventor Name Search Result**

Your Search was:

Last Name = SUNG

First Name = NAK HYUN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09473470	6320221	150		TFT-LCD HAVING A VERTICAL THIN FILM TRANSISTOR	SUNG, NAK HYUN
10620716	Not Issued	71		Chip-on-glass type liquid crystal display	SUNG, NAK HYUN

Inventor Search Completed: No Records to Display.

Correl Arrethan Immedia	Last Name	First Name	
Search Another: Inventor	SUNG	NAK HYUN	Search

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